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UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6321074

<=2> Get Drawing Sheet 1 of 2

November 20, 2001

Apparatus and method for reducing oscillator frequency pulling during AM modulation

REISSUE: November 19, 2003 - Reissue Application filed Ex. Gp.: 2684; Re. S.N.

10/717,426 (O.G. February 17, 2004)

APPL-NO: 336912 (09)

FILED-DATE: June 21, 1999

GRANTED-DATE: November 20, 2001

CORE TERMS: frequency, pulling, modulation, filter, synthesizer, oscillator,

spurious, mixer, divider, architecture ...

LEXIS-NEXIS Library: PATENT

File: ALL

6,321,074 OR 6321074

LEXIS-NEXIS
Library: PATENT
File: CASES

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6,321,074 OR 6321074

LEXIS-NEXIS Library: NEWS File: CURNWS

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For further explanation, press the H key (for HELP) and then the ENTER key.

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4/39/1
DIALOG(R)File 345:Inpadoc/Fam.& Legal Stat
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19708188
Basic Patent (No, Kind, Date): CA 2298926 AA 20000818
                                                         <No. of Patents: 011>
Patent Family:
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                                               Kind Date
    Patent No
                 Kind Date
                                      CA 2298926
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                                                            20000217 (BASIC)
                  AA 20000818
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C 20030729 CA 2298928 A 20000217
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BA 20011120 US 336912 A 19990621
BA 20020416 US 433331 A 19991103
BA 20020709 US 491538 A 20000126
    CA 2298992
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    US 6297708
    US 6321074
    US 6373236
    US 6417729
Priority Data (No, Kind, Date):
    US 120641 P 19990218
    US 433331 A 19991103
    US 336912 A 19990621
    US 491538 A 20000126
    US 325050 A 19990603
    US 506169 A 20000217
PATENT FAMILY:
CANADA (CA)
  Patent (No, Kind, Date): CA 2298926 AA 20000818
   TEMPERATURE COMPENSATED POWER DETECTOR DETECTEUR DE PUISSANCE A
      COMPENSATION DE TEMPERATURE (English; French)
    Patent Assignee: ITRON INC (US)
    Author (Inventor): SADOWSKI ERIC (US); LEMAY NORMAND T JR (US)
    Priority (No, Kind, Date): US 120641 P 19990218; US 433331 A
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    Applic (No, Kind, Date): CA 2298926 A
                                              20000217
    IPC: * G01R-021/14
    Language of Document: English
  Patent (No, Kind, Date): CA 2298927 AA 20000818
    APARATUS AND METHOD FOR REDUCING OSCILLATOR FREQUENCY PULLING DURING AM
      MODULATION APPAREIL ET METHODE POUR REDUIRE L'ENTRAINEMENT DE
      FREQUENCE EN MODULATION D'AMPLITUDE (English; French)
   Patent Assignee: ITRON INC (US)
    Author (Inventor): LEMAY NORMAND T JR (US)
    Priority (No, Kind, Date): US 120641 P 19990218; US 336912 A
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    Applic (No, Kind, Date): CA 2298927 A
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  Patent (No, Kind, Date): CA 2298928 AA 20000818
    LINEAR POWER CONTROL LOOP BOUCLE DE COMMANDE LINEAIRE DE PUISSANCE
       (English; French)
    Patent Assignee: ITRON INC (US)
    Author (Inventor): LOFSTAD ERIC W (US); MACCONNELL JOHN W (US);
      SADOWSKI ERIC (US); BRUNN BRIAN T (US); LEMAY NORMAND T JR (US)
    Priority (No, Kind, Date): US 120641 P 19990218; US 491538 A
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   CONSTANT A COMPENSATION DE TEMPERATURE (English; French)
   Patent Assignee: ITRON INC
                                (US)
   Author (Inventor): LEMAY NORMAND T JR
                                            19990218; US 325050 A
   Priority (No, Kind, Date): US 120641 P
     19990603
   Applic (No, Kind, Date): CA 2298929 A
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   Language of Document: English
 Patent (No, Kind, Date): CA 2298992 AA 20000818
   TEMPERATURE COMPENSATED HIGH PERFORMANCE OSCILLATOR OSCILLATEUR A
     COMPENSATION THERMIQUE A HAUT RENDEMENT (English; French)
   Patent Assignee: ITRON INC
                                (US)
   Author (Inventor): LEMAY NORMAND T JR
                                            (US)
   Priority (No, Kind, Date): US 120641 P
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   Applic (No, Kind, Date): CA 2298992 A
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   TEMPERATURE COMPENSATED POWER DETECTOR DETECTEUR DE PUISSANCE A
      COMPENSATION DE TEMPERATURE (English; French)
   Patent Assignee: ITRON INC (US)
Author (Inventor): LEMAY NORMAND T JR (US); SADOWSKI ERIC (US)
    Priority (No, Kind, Date): US 120641 P
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  Patent (No, Kind, Date): CA 2298928 C
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      (English; French)
   Patent Assignee: ITRON INC
                                 (US)
   Author (Inventor): LOFSTAD ERIC W (US); SADOWSKI ERIC
      MACCONNELL JOHN W (US); BRUNN BRIAN T (US); LEMAY NORMAND T JR
                                            19990218; US 491538 A
    Priority (No, Kind, Date): US 120641 P
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    Applic (No, Kind, Date): CA 2298928 A
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    IPC: * H03G-003/20
    Language of Document: English
CANADA (CA)
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                        20030403 CA EEER
                              DATE: 20010504
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UNITED STATES OF AMERICA (US)

20000126

Patent (No, Kind, Date): US 6297708 BA 20011002

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Temperature compensated high performance oscillator (English)
   Patent Assignee: ITRON INC (US)
   Author (Inventor): LEMAY NORMAND T
   Priority (No, Kind, Date): US 506169 A
                                          20000217; US 120641 P
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   Applic (No, Kind, Date): US 506169 A
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   IPC: * H03B-005/00
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 Patent (No, Kind, Date): US 6321074 BA 20011120
   Apparatus and method for reducing oscillator frequency pulling during
     AM modulation (English)
   Patent Assignee: ITRON INC
                               (US)
   Author (Inventor): LEMAY NORMAND T
                                           19990621; US 120641 P
   Priority (No, Kind, Date): US 336912 A
     19990218
   Applic (No, Kind, Date): US 336912 A
                                         19990621
   National Class: * 455260000; 455108000; 455075000; 455076000;
     455112000; 455118000; 455119000; 332363000; 332127000
   IPC: * H04B-001/06
   Language of Document: English
  Patent (No, Kind, Date): US 6373236 BA 20020416
   Temperature compensated power detector (English)
   Patent Assignee: ITRON INC (US)
                                          (US); SADOWSKI ERIC
   Author (Inventor): LEMAY JR NORMAND T
   Priority (No, Kind, Date): US 433331 A 19991103; US 120641
   19990218
                                        19991103
   Applic (No, Kind, Date): US 433331 A
   National Class: * 324095000; 324648000; 324600000; 324685000;
     324721000; 324072500; 323369000; 323907000
    IPC: * G01R-023/04; G01R-027/04; G01R-027/00; G01R-027/26; H03M-001/00
    Language of Document: English
  Patent (No, Kind, Date): US 6417729 BA 20020709
   Linear power control loop (English)
    Patent Assignee: !ITRON INC (US)
    Author (Inventor): LEMAY NORMAND T (US); BRUNN BRIAN T (US);
     MACCONNELL JOHN W (US); SADOWSKI ERIC (US); LOFSTAD ERIC W
    Priority (No, Kind, Date): US 491538 A 20000126; US 120641 P
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    National Class: * 330129000; 330140000
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    Language of Document: English
UNITED STATES OF AMERICA (US)
  Legal Status (No, Type, Date, Code, Text):
  US 6297708 P 19990218 US AA PRIORITY (US PROVISIONAL
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                                            19990218
                             US 120641 P
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    US 6297708
                       20000217 US AE
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    US 6297708
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                             T.; REEL/FRAME: 010571/0522
                                             PATENT (NO PREVIOUS
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    US 6297708
                             PRE-GRANT PUBLICATION)
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    US 6297708
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	US	6321074	P	19990621 US AE APPLICATION DATA (PATENT)
				(APPL. DATA (PATENT)) US 336912 A 19990621
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	US	6321074	P	PRE-GRANT PUBLICATION)
			_	PRE-GRANT PUBLICATION/
	US	6321074	P	20040217 US RF REISSUE APPLICATION FILED
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. 1	US	6373236	P	
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				T.JR.;SADOWSKI, ERIC;REEL/FRAME:010374/0178
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	US	6417729	P	19990218 US AA PRIORITY (US PROVISIONAL
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	US	6417729	P	20000126 US AE APPLICATION DATA (PATENT)
				(APPL. DATA (PATENT))
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	US	6417729	P	20000424 US AS ASSIGNMENT
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				INTEREST; ASSIGNORS: LEMAY, NORMAND T.; BRUNN,
				BRIAN T.; MACCONNELL, JOHN W.; AND
		•		BRIAN T.; MACCONNELL, JOHN W.; AND OTHERS; REEL/FRAME: 010727/0096; SIGNING DATES
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